

EAST SEARCH

1/14/2009

L#	Hits	Search String	Databases
L1	26	((logic and gate and delay adj time) and rise and fall) and logical adj operation) an	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L2	5	((logic and gate and delay adj time) and rise and fall) and logical adj operation) an	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L3	8	((logic and gate and delay adj time) and rise and fall) and logical adj operation) an	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L4	46970	hasegawa.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L5	956	hasegawa.in. and delay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L6	121	(hasegawa.in. and delay) and NEC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L7	45	(hasegawa.in. and delay) and NEC	USPAT
L1	12	((hasegawa.in. and delay) and NEC) and rise and fall	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L2	1628	delay adj calculat\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L3	26127	look adj3 table	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L4	74	(delay adj calculat\$) and (look adj3 table)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L5	3	((delay adj calculat\$) and (look adj3 table)) and library	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L6	473	(delay adj calculat\$) and gate	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L7	67	((delay adj calculat\$) and gate) and fall and rise	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L8	38	(((delay adj calculat\$) and gate) and fall and rise) and simulat\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L9	29	((Blinne and delay time) and logic cell) and rise/fall) and estimating	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
	7	optimizing adj signal adj timing	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
106402		logic adj circuit\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
310		(logic adj circuit\$1) and (calculat\$3 adj delay)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
37		((logic adj circuit\$1) and (calculat\$3 adj delay)) and (logic\$2 adj (information or op	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
112		(logic adj circuit\$1) and (comput\$5 adj delay)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
96		(logic adj circuit\$1) and (estimat\$3 adj delay)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
468		((logic adj circuit\$1) and (calculat\$3 adj delay)) or ((logic adj circuit\$1) and (compur	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
56		((logic adj circuit\$1) and (calculat\$3 adj delay)) or ((logic adj circuit\$1) and (compl	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
7		((logic adj circuit\$1) and (calculat\$3 adj delay)) and (logic\$2 adj (information or op	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
5		((logic adj circuit\$1) and (calculat\$3 adj delay)) or ((logic adj circuit\$1) and (comp	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
11		((logic adj circuit\$1) and (calculat\$3 adj delay)) or ((logic adj circuit\$1) and (comp	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
33722		logic adj gate\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
179		(logic adj gate\$1) and (calculat\$3 adj delay)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
47		(logic adj gate\$1) and (comput\$5 adj delay)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
61		(logic adj gate\$1) and (estimat\$3 adj delay)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
268		((logic adj gate\$1) and (calculat\$3 adj delay)) or ((logic adj gate\$1) and (comput\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
38		((logic adj gate\$1) and (calculat\$3 adj delay)) or ((logic adj gate\$1) and (comput\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
0		((logic adj gate\$1) and (calculat\$3 adj delay)) or ((logic adj gate\$1) and (comput\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
220		(logic adj circuit\$1) and (delay with library)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
46		((logic adj circuit\$1) and (delay with library)) and ("connection information" or "circu	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
0		((logic adj circuit\$1) and (delay with library)) and ("connection information" or "circ	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
10		((logic adj circuit\$1) and (delay with library)) and ("connection information" or "circ	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
220		(logic adj circuit\$1) and (delay with library)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
11		((logic adj circuit\$1) and (delay with library)) and "logic information"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB